# ComPar: Optimized Multi-Compiler for Automatic OpenMP S2S Parallelization

Idan Mosseri, <u>Lee-or Alon</u>, Re'em Harel and Gal Oren







# From Sequential to Parallel

Excellent performance in theory, time consuming in practice

Growing usage in multi-core architect-ures

From wearable devices through personal computers to HPC

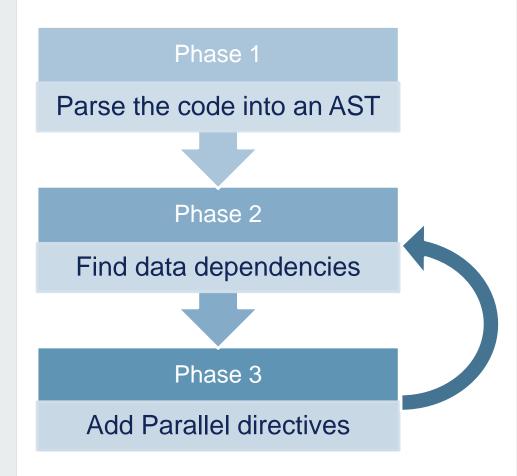
One must have a deep understanding of the code and be very cautious not to change the inner logic

To fully exploit these systems, the code has to be adjusted

Automatic S2S Parallelization compilers To ease this difficult process, automatic S2S compilers were introduced

# From Sequential to Parallel

Automatic source-to-source parallelization compilers



# From Sequential to Parallel

Automatic source-to-source parallelization compilers

Currently, no
existing automatic
parallelization
compiler can fully
replace the
programmer's
insight

There is NO
best S2S automatic
parallelization
compiler
Each has its
advantages and
disadvantages

## Compilers Comparison

There is no best S2S automatic parallelization compiler In [1], we concluded that the most suitable compilers for our task would be AutoPar, Par4All and Cetus

 Mostly because they are free up-to-date S2S compilers Other S2S automatic parallelization compilers can be easily added to ComPar

 It just needs to implement ComPar's API

[1] Harel, Re'em, et al. "Source-to-source parallelization compilers for scientific shared-memory multi-core and accelerated multiprocessing: Analysis, pitfalls, enhancement and potential." International Journal of Parallel Programming 48.1 (2020): 1-31.

## AutoPar

#### Background

- Developed by Lawrence Livermore National Laboratory
- For C and C++ programs
- A module within the ROSE compiler
- Open-source

#### (main) Pros

- Inherently suitable for OOP
- Handles nested loops
- Verifies existing OMP directives in a given code
- Can be directed to add OMP directives regardless of errors
- Modifications are accompanied by explanation in its output

#### (main) Cons

- Requires programmer intervention to handle function side-effects etc
- Lacks the ability to tune the parallelization directives for each level in the nested loop
- May add incorrect OpenMP directives when given the "No-aliasing" option

## Par4All

#### Background

- Developed by SILKAN, MINES ParisTech, and Institute Télécom
- For C and Fortran programs
- Open-source
- Its development was shut-down by 2015.

#### (main) Pros

- Automatically analyzes function side effects and pointer aliasing
- Suitable for GPUs
- Supports many data types
- Supports

   Fortran (hence more suitable for scientific legacy codes)

#### (main) Cons

- May change the code structure
- Unused functions will not be parallelized

## Cetus

#### Background

- Developed by ParaMount research group at Purdue University
- For C programs
- Open-source
- Contains a GUI and a clientserver model

#### (main) Pros

- Handles nested loops
- Provides crossplatform interface
- Verifies existing OMP directives in a given code
- Modifications are accompanied by explanation in its output
- Loop size dependent parallelization

#### (main) Cons

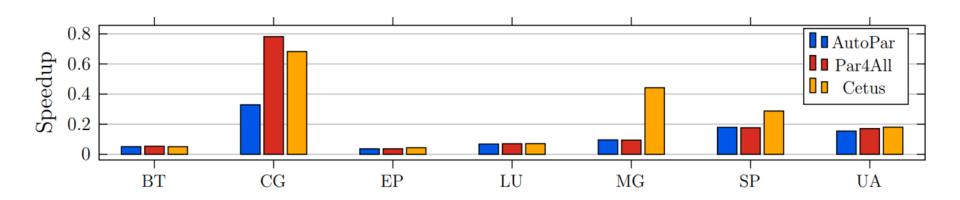
- Adds its own pragmas which create excess code
- May create reduction clauses that are unknown for standard compilers
- Does not insert OMP directives to loops that contain function calls

## **Compilers Comparison**

Feature	AutoPar (ROSE)	Par4AII (PIPS)	Cetus
Loop unrolling	No	Yes	Yes
Supported languages	C, C++	C, Fortran, CUDA	С
"No-aliasing" option	Yes	Yes	Yes
Check alias dependence	No	Yes	Yes
Reduction clauses	Yes	Yes	Yes
Array reduction/privatization	No	No	Yes
Nested loops	Yes	No	Yes
Function side effect	Annotation required	Yes	Yes
OOP compatible	Yes	No	No
Development status	Yes	No	Yes

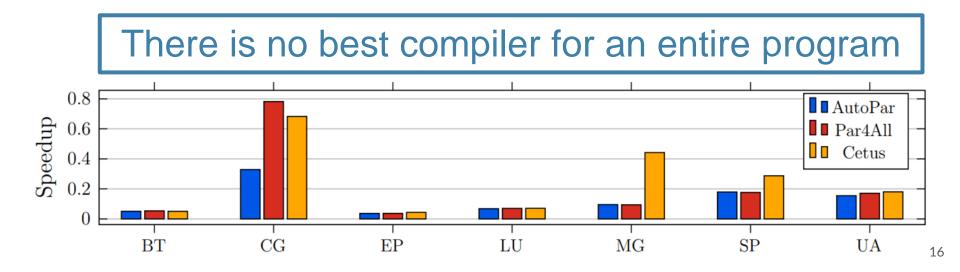
### **NAS Parallel Benchmarks**

- Numerical Aerodynamics Simulations (NAS) Parallel Benchmarks
- Developed by NASA
- Evaluate the performance of HPC



#### **NAS Parallel Benchmarks**

- There is a compiler for a suitable-for-parallelization individual segment
- Using only one compiler at a time is not enough to fully exploit the hardware capabilities to the limit
- Carefully fuse the abilities of all compilers



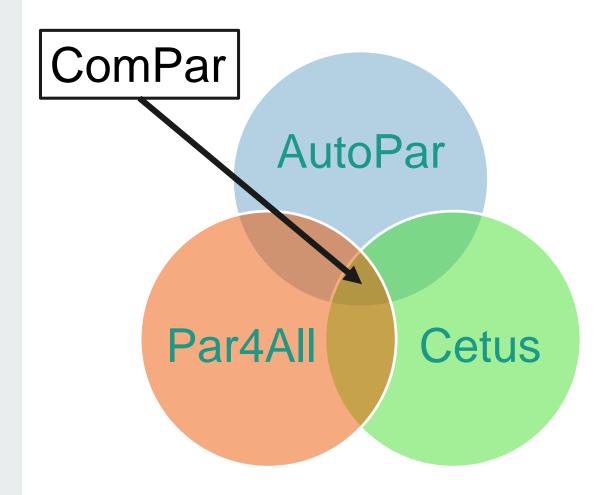
Enjoy the best of all worlds

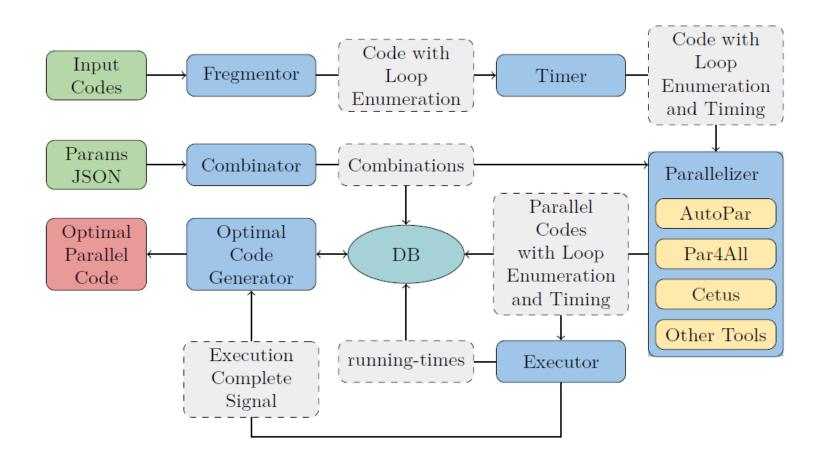
S2S compiler that
optimizes a code
parallelization that
can be achieved from
S2S automatic
parallelization
compilers without
any human
intervention

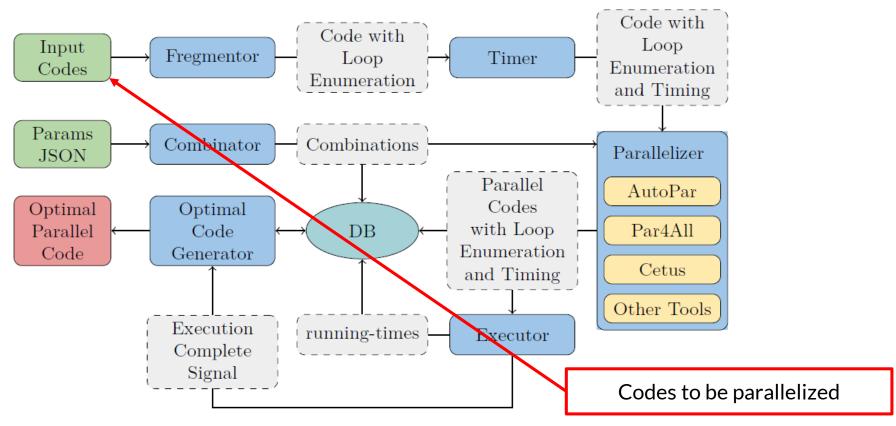
Fusing several outputs of said compilers while selecting the best from each

User only has to specify the desired hyperparameters to be considered (in a JSON file)

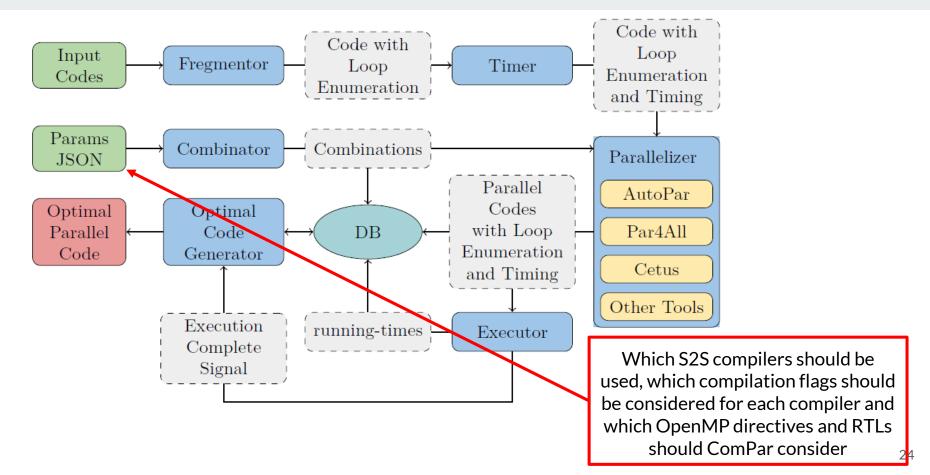
In other words

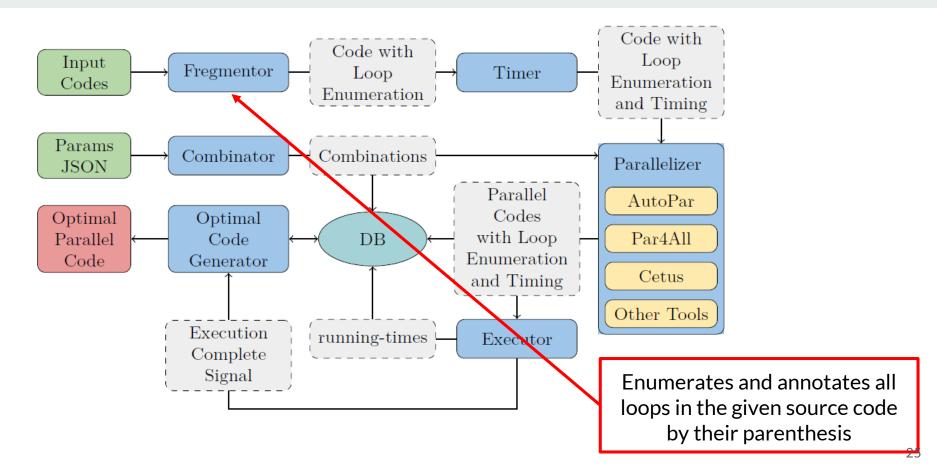


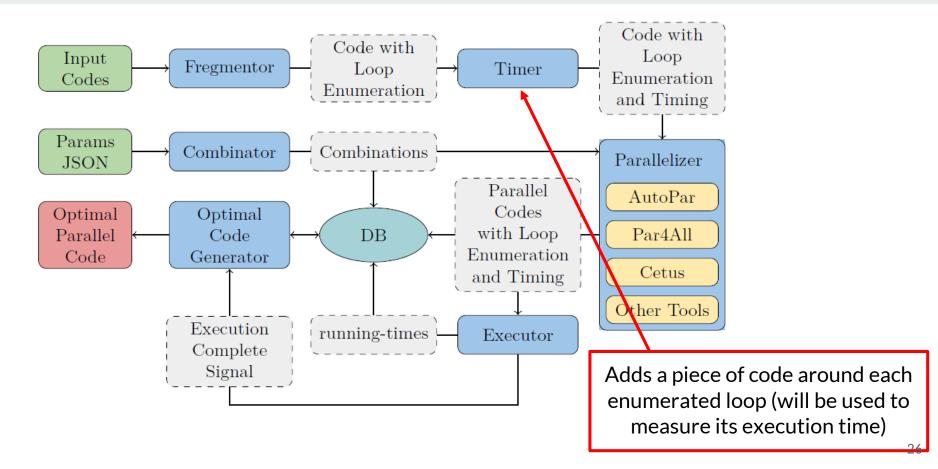


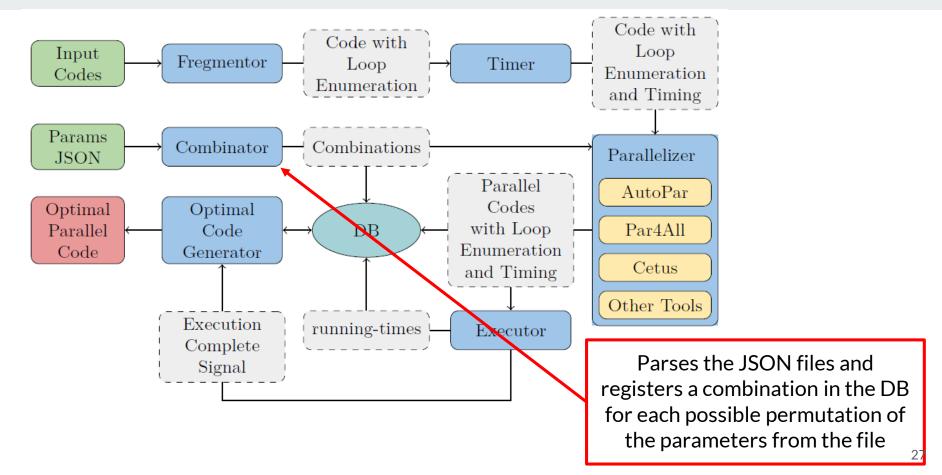


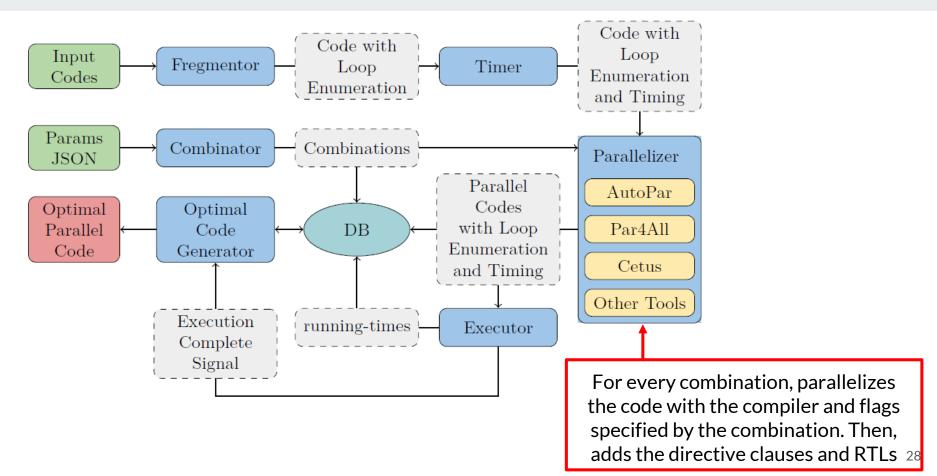
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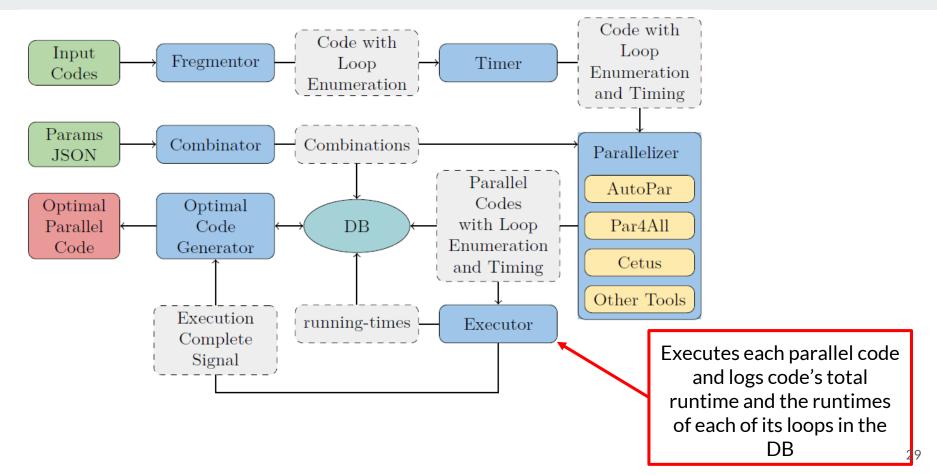


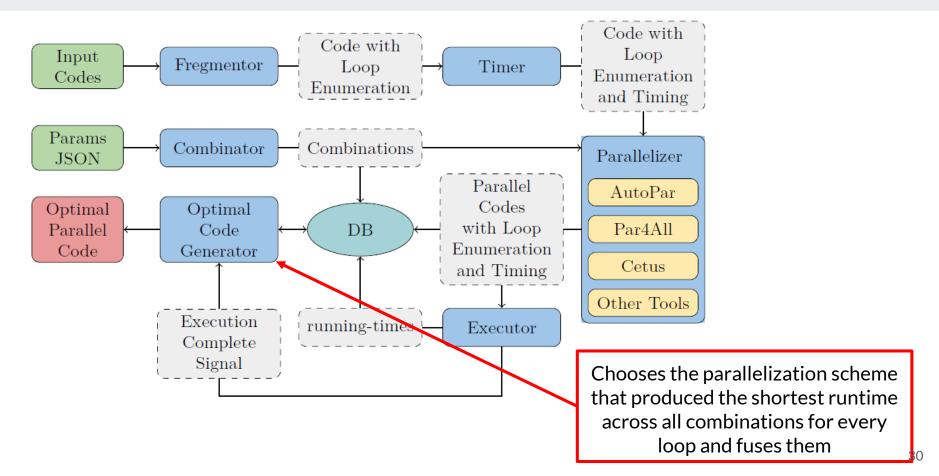


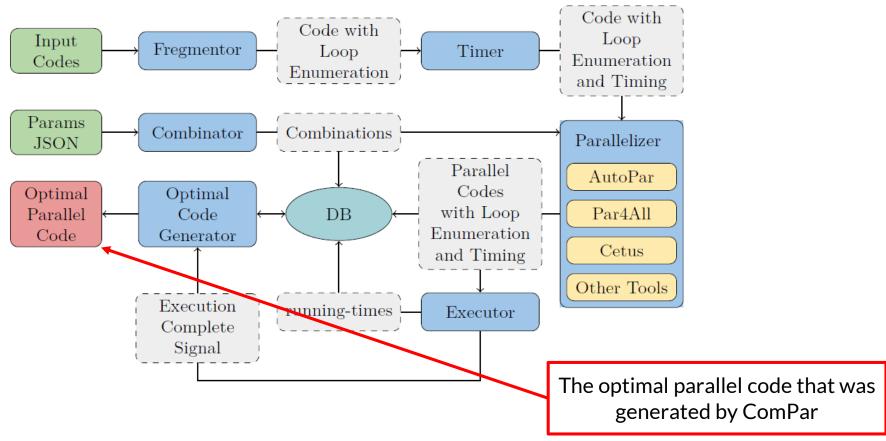












Correctness of the generated program

- To validate the correctness of the generated code, ComPar uses black-box testing
  - Examines the application before and after the parallelization without peering into its internal structures or workings
- ComPar rejects any combination that did not pass the tests

#### Listing 1 Daxpy Serial Code

```
void init(double *x, double *y, int N){
        for (int i=0: i<N: ++i) {
            x[i] = i*0.3; y[i] = i*0.2;
    int main() {
        double *x, *y, alpha = 3.0;
        int N = 1024*1024*256, R = 64, k,i,g;
        x = (double *) malloc (N *)

    sizeof(double)):
        y = (double *) malloc (N *)

    sizeof(double));

        init(x,y,N);
10
        for(k=0; k<R; k++)
11
            for (i=0; i<N; i++)
                y[i] = alpha * x[i];
13
```

#### Listing 2 ComPar's Parallel Optimized Daxpy

```
void init(double *x, double *y, int N) {
        // START LOOP MARKER1 | COMB ID: HASH#1 | COMPILER NAME: autopar
        #pragma omp parallel for firstprivate(N)
       for (int i = 0; i \le N - 1; i += 1) {
            x[i] = i * 0.3; v[i] = i * 0.2;  // END LOOP MARKER1
   int main() {
        double *x, *y, alpha = 3.0;
        int N = 1024 * 1024 * 256, R = 64, k, i, g;
        x = (double *)malloc(N * sizeof(double));
       y = (double *)malloc(N * sizeof(double));
        init(x, y, N);
12
        // START_LOOP_MARKER2 | COMB_ID: HASH#2 | COMPILER_NAME: cetus
13
        #pragma cetus firstprivate(y) private(i, k) lastprivate(y)
14
        #pragma loop name main #0
15
        #pragma cetus parallel
16
        \#pragma omp parallel for if ((10000 < ((1L + (3L * R)) + ((3L * N)) *
17
        → R)))) private(i, k) firstprivate(y) lastprivate(y)
       for (k = 0; k < R; k++) {
            #pragma cetus private(i)
            #pragma loop name main #0 #0
            #pragma cetus parallel
            #pragma omp parallel for if ((10000 < (1L + (3L * N))))
            \rightarrow private(i)
            for (i = 0: i < N: i++) {
23
                y[i] = (alpha * x[i]);}} // END_LOOP_MARKER2
24
25
```

#### Listing 1 Daxpy Serial Code

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void init(double *x, double *y, int N){
       for (int i=0: i<N: ++i) {</pre>
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       double *x, *y, alpha = 3.0;
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       x = (double *) malloc (N *)

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       y = (double *) malloc (N *)

    sizeof(double));

       init(x,y,N);
10
       for(k=0; k<R; k++)
            for (i=0; i<N; i++)
                y[i] = alpha * x[i];
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#### Listing 2 ComPar's Parallel Optimized Daxpy

```
void init(double *x, double *y, int N) {
        // START LOOP MARKER1 | COMB ID: HASH#1 | COMPILER NAME: autopar
        #pragma omp parallel for firstprivate(N)
       for (int i = 0; i \le N - 1; i += 1) {
            x[i] = i * 0.3; v[i] = i * 0.2;  // END LOOP MARKER1
    int main() {
        double *x, *y, alpha = 3.0;
        int N = 1024 * 1024 * 256, R = 64, k, i, g;
        x = (double *)malloc(N * sizeof(double));
        v = (double *)malloc(N * sizeof(double));
        init(x, y, N);
12
        // START_LOOP_MARKER2 | COMB_ID: HASH#2 | COMPILER_NAME: cetus
13
        #pragma cetus firstprivate(y) private(i, k) lastprivate(y)
        #pragma loop name main #0
15
        #pragma cetus parallel
16
        \#pragma omp parallel for if ((10000 < ((1L + (3L * R)) + ((3L * N)) *
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        \rightarrow R)))) private(i, k) firstprivate(y) lastprivate(y)
        for (k = 0; k < R; k++) {
            #pragma cetus private(i)
19
            #pragma loop name main #0 #0
            #pragma cetus parallel
            #pragma omp parallel for if ((10000 < (1L + (3L * N))))
            \rightarrow private(i)
            for (i = 0: i < N: i++) {
23
                y[i] = (alpha * x[i]);}} // END_LOOP_MARKER2
24
```

#### Listing 1 Daxpy Serial Code

```
void init(double *x, double *y, int N){
        for (int i=0: i<N: ++i) {</pre>
            x[i] = i*0.3; y[i] = i*0.2;
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    sizeof(double));

        init(x,y,N);
10
        for(k=0; k<R; k++)
            for (i=0; i<N; i++)
                y[i] = alpha * x[i];
13
```

#### Listing 2 ComPar's Parallel Optimized Daxpy

```
void init(double *x, double *y, int N) {
        // START LOOP MARKER1 | COMB ID: HASH#1 | COMPILER NAME: autopar
        #pragma omp parallel for firstprivate(N)
       for (int i = 0; i <= N - 1; i += 1) {
            x[i] = i * 0.3; v[i] = i * 0.2;  // END LOOP MARKER1
    int main() {
        double *x, *y, alpha = 3.0;
        int N = 1024 * 1024 * 256, R = 64, k, i, g;
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        v = (double *)malloc(N * sizeof(double));
        init(x, y, N);
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        // START_LOOP_MARKER2 | COMB_ID: HASH#2 | COMPILER_NAME: cetus
13
        #pragma cetus firstprivate(y) private(i, k) lastprivate(y)
        #pragma loop name main #0
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        \#pragma omp parallel for if ((10000 < ((1L + (3L * R)) + ((3L * N)) *
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        for (k = 0; k < R; k++) {
            #pragma cetus private(i)
            #pragma loop name main #0 #0
            #pragma cetus parallel
            #pragma omp parallel for if ((10000 < (1L + (3L * N))))
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            for (i = 0: i < N: i++) {
23
                y[i] = (alpha * x[i]);}} // END_LOOP_MARKER2
24
```

File	daxpy.c		py.c
Comb'	HASH	[# <i>1</i>	HASH#2
Comp'	Autopar		Cetus
Comp'	keep_	going	parallelize- loops=2
Flags	no_ali	0	privatize=2 alias=3
Runtime	0.11		1.99
Speedup	16.62		26.96
Total (sec)	2.21		

```
Listing 2 ComPar's Parallel Optimized Daxpy
   void init(double *x, double *y, int N) {
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        #pragma omp parallel for firstprivate(N)
       for (int i = 0; i \le N - 1; i += 1) {
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        int N = 1024 * 1024 * 256, R = 64, k, i, g;
        x = (double *)malloc(N * sizeof(double));
        v = (double *)malloc(N * sizeof(double));
        init(x, y, N);
        // START LOOP MARKER2 | COMB ID: HASH#2 | COMPILER NAME: cetus
13
        #pragma cetus firstprivate(y) private(i, k) lastprivate(y)
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        #pragma loop name main #0
15
        #pragma cetus parallel
16
        #pragma omp parallel for if ((10000 < ((1L + (3L * R))) + ((3L * N)) *
        → R)))) private(i, k) firstprivate(y) lastprivate(y)
       for (k = 0; k < R; k++) {
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            \rightarrow private(i)
            for (i = 0; i < N; i++) {
23
                y[i] = (alpha * x[i]);}} // END_LOOP_MARKER2
24
25
```

At worst case, it will be as good as the most suitable compiler for the given code

# ComPar support is limited to the compilers it uses

- ComPar can parallelize over accelerators, because AutoPar can
- The chosen compilers are limited to OMP v2.5, hence ComPar cannot benefit from the advantages of later versions

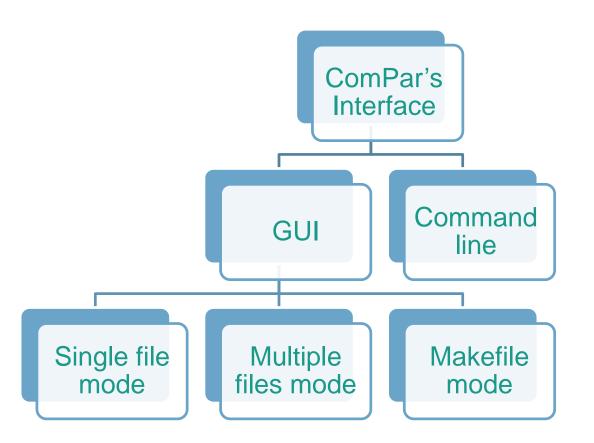
Problem size

Runtime depends on the runtime of the given source code

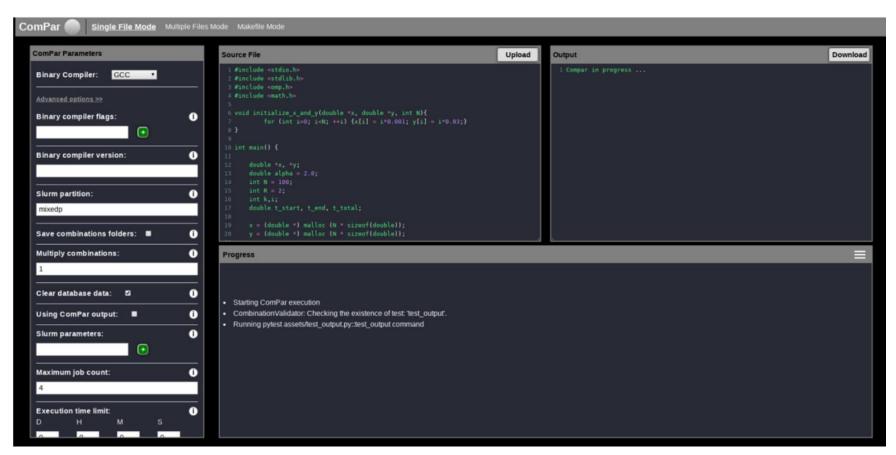
It would be wise to choose a 'sweet-spot'

Then run the real input using the parallel code generated

Interface



## Single File Mode



## **Multiple Files Mode**



## **Makefile Mode**

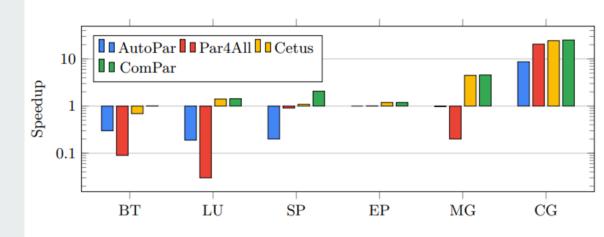


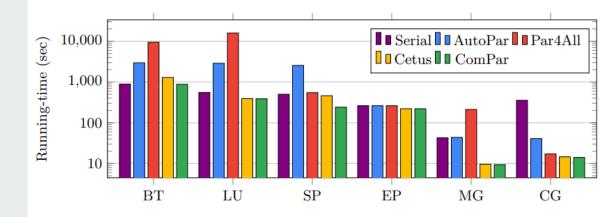
Parameters used in the experiments

Compilers' Flags				
Compiler	Flag			
Cetus	parallelize-loops, reduction, private, alias			
AutoPar	keep_going, enable_modeling, no_aliasing, unique_indirect_index			
Par4All	O, fine-grain, com-optimization, no-pointer-aliasing			
OMP parallel for Directive Clauses				
Clause	Kind			
schedule	static [2,4,8,16,32], dynamic			
Runtime Library Routines				
RTL Routine	Argument			
omp_set_num_threads	2,4,8,16,32			

NAS

The results are the best each S2S compiler achieved using different flags combinations -- **not a** "vanilla" execution





PolyBench

- 30 representative potentially compute-intensive benchmarks
- Attempts to make the kernels' execution as uniform and consistent as possible
- We enlarged the (already LARGE) problem size by x8

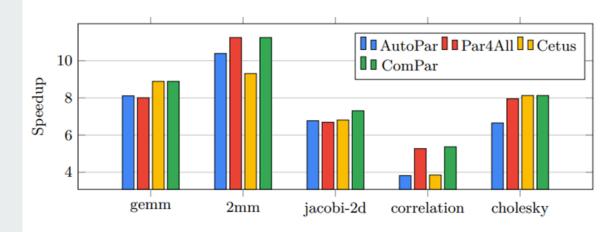
PolyBench

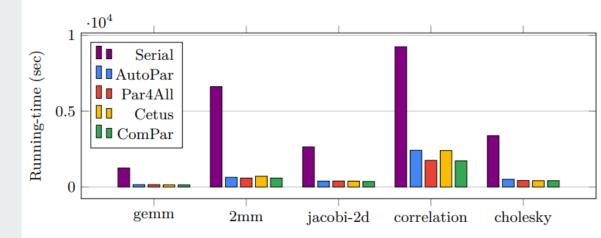
Linear Algebra Kernels		Medley
2mm	3mm	deriche
atax	bicg	floyd-warshall
doitgen	mvt	nussinov
Linear Algebra Solvers		BLAS Routines
cholesky	durbin	gemm
gramschmidt	lu	gemver
ludcmp	trisolv	gesummv
Stencils		symm
adi	ftdt-2d	syrk
heat-3d	jacobi-1d	syr2k
jacobi-2d	seidel-2d	trmm
Data Mining	covariance	correlation

50

PolyBench

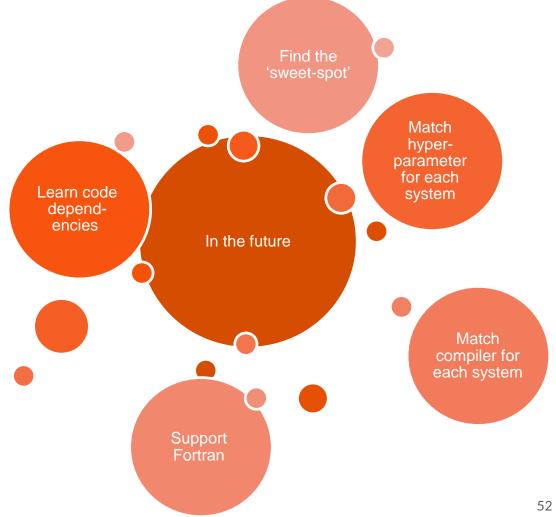
The results are the best each S2S compiler achieved using different flags combinations -- **not a** "vanilla" execution





### **Future Work**

Much work is left...



## **Summary**

What have we seen today?

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- To enjoy multi-core architectures, one must adjust its code
  - Very complicated!
- To ease this problem, automatic S2S parallelization compilers were introduced

No compiler is superior to all other compilers in all tests

• Carefully fuse the abilities of all compilers

ComPar allows users to enjoy the advantages of these compilers, while avoiding, when possible, from their disadvantages